Position-Dependent Arrays and Their Application for High Performance Code Generation

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Abstract
Modern parallel hardware promises unprecedented performance, for the gifted few experts who can program it correctly. Code generators from high-level languages provide an attractive alternative, promising to deliver high performance automatically. Existing projects such as Accelerate, Futhark, Halide, or Lift show that this approach is feasible. Unfortunately, existing efforts focus on computations over tensors: regularly shaped higher dimensional arrays. This limits the expressiveness of these approaches and excludes many interesting data structures that are commonly encoded manually in memory, such as trees or triangular matrices.

This paper presents an extended array type that lifts this restriction. For multidimensional arrays, the size of a nested array might depend on its position in the surrounding arrays, which enables the expression of computations over less regularly shaped data structures. However, these position-dependent arrays bring new challenges for high-performance code generation, as determining the position of the elements in memory becomes more challenging.

This paper shows how these challenges are addressed by extending the existing Lift type system and compiler. The experimental results show that this approach enables the efficient code generation of triangular matrix-vector multiplication, with performance improvements over cuBLAS on an Nvidia GPU by up to 2x. Furthermore, we show a use case for a low-level optimization for avoiding unnecessary out-of-bound checks in stencils, leading to up to 3x improvements over already optimized generated stencil codes.

1 Introduction
Domain specific code generators enables the generation of efficient parallel code from high-level abstractions. These code generators attempt to fulfill the high-performance needs of many domains, such as machine learning, that crucially rely on the efficient exploitation of high-performance hardware such as GPUs. It is extremely challenging, even for experts, to write correct and efficient programs in low-level programming approaches such as CUDA or OpenCL. Code generation from high-level abstractions offers an attractive alternative and recent research has provided significant advances with projects such as Accelerate [13], Futhark [11], Halide [14], and Lift [17, 18]. There has been particular interest from industry in high performance code generations for deep learning with projects such as Tensor Comprehensions [19], Glow [16] for compiling PyTorch networks, as well as XLA [8] for compiling TensorFlow graphs.

The focus of existing work in this area has been on computations over regularly shaped higher-dimensional arrays, know as tensors. Many important application domains fall into this category, but there also exist many important applications that require more irregularly shaped data structures. For instance, triangular matrices are extremely important in many fields, as they are commonly used to perform efficient inversion of symmetric matrices [3]. Moreover, many physical phenomena can be modelled using matrices that have unusual characteristics, such as banded matrices, or even more exotic type of matrices [7].

For such applications, irregularly shaped data must currently be encoded manually in the provided regular-shaped arrays. This leads to increased complexity for the programmer, possible inefficiencies in memory usage and compute time, and ultimately defies the purpose of a high-level code generator. Encoding irregularly shaped data explicitly in memory is well known to low-level programmers who are forced to manually encode higher-level data structures in flat memory buffers.

In this paper, we present our approach to generate efficient parallel code for irregularly shaped data by extending the functional Lift intermediate representation and code generator. Crucially, we show how an extension of the type system with a limited form of dependent types enables us to generate efficient parallel code for computations over irregularly shaped data, without much changes in the way the Lift code generator operates. We also tackle the challenges that such irregularly shaped data bring for memory access calculation when indexing elements.

High-level Lift programs are composed of well known high-level primitives such as map or reduce. Such programs are transformed by the Lift compiler into a low-level form using a set of rewrite rules that are applied in an automated optimization process. Finally, efficient parallel code is generated from an optimized low-level program. Our extension to Lift ensures that existing primitives continue to work...
over irregularly shaped arrays. Furthermore, we add a new primitive — *partition* — to break down a one-dimensional array into an irregularly shaped two-dimensional structure. This primitive is useful for encoding lower level optimizations, such as avoiding out-of-bound checks for stencil codes. Our implementation carefully extends Lift to reuse as much existing infrastructure as possible.

Our experimental results demonstrates that the extended Lift code generator is capable of generating efficient parallel GPU code for a number of important use-cases operating on irregular data. For triangular matrix-vector multiplication, a crucial numerical kernel included in BLAS, we achieve performance on par with cuBLAS on a Nvidia GPU and even a speedup of 2× for certain input sizes. By using the *partition* primitive, we improve the performance of GPU stencil code by avoiding out-of-bound checks resulting in up to 3× improvements for certain stencil sizes.

To summarize the contributions of this paper:

- We present a generalization of array types capable of representing irregularly shaped data such as triangular arrays and discuss our design and implementation, including a *partition* primitive for introducing irregularity into regular arrays (Section 4);
- We show how we generate efficient array indices using symbolic simplification extended to deal with the position dependent arrays (Section 5);
- We present performance results for two case studies demonstrating that our approach generates efficient parallel code with performance improvements of up to 2× compared to the *trmv* kernel in cuBLAS as well as performance improvements of up to 3× over already optimized stencil code by automatically avoiding unnecessary boundary checks (Section 6).

We first start with a motivation (Section 2) and background information about traditional array types (Section 3).

## 2 Motivation

The ever growing demand of increased performance is fueling the development of domain specific code generators to automatically generate high performance code from high level notations. Academic projects such as Accelerate [13], Futhark [11], or Lift [17, 18] use functional languages as compiler intermediate representation for high performance code generators. This approach has the advantage that code generation is guided by a strong formal foundation, including a type system and formal semantics.

Lift, for example, tracks the size of multi-dimensional array dimensions in the type system and uses this information for the generation of loop bounds and array indices. This allows for a high level notation that omits details such as indexing arrays while typing guarantees that the information required for generating correct array indices in the low level program are always accessible. Similarly, Accelerate and Futhark track the shape of multidimensional arrays.

The representation of regularly shaped multidimensional arrays in a type system is well known in the functional programming community and it is well understood how such arrays are represented when flattened in memory. However, the type systems used for functional intermediate representations are so far not expressive enough to represent other useful less regular data multi-dimensional structures such as triangular matrices. Triangular matrices for instance, are used when dealing with some classes of partial differential equations and least square problems, and are commonly used to represent systems of equations, as discussed by de Castro Martins et al. [6].

In this paper, we investigate how multi-dimensional arrays representing less regularly shaped data are represented at the type level and how to generate high performance code for them. We look at two use cases of computations in detail: triangular matrix vector multiplication and the use of irregularly shaped array as a compiler internal data structure to optimize avoidance of out-of-bound checks for stencils. These two very different use cases have been chosen to highlight the potential of our generic approach.

### 2.1 Use-case 1: Triangular matrix vector multiplication

Triangular matrices naturally appear in different areas of mathematics, for example when solving linear equations. Triangular matrix vector multiplication is a fundamental building block included in the basic linear algebra subroutines (BLAS) API in form of the *trmv* kernel. Figure 1 shows a visualization of the operation. Since the matrix has a triangular shape, for each row *i* the dot product is computed only with the first *i* elements of the vector.

In current systems such as Accelerate, Futhark, or Lift, it is unclear how a triangular matrix should be represented, as their type systems are not expressive enough to precisely represent a triangular matrix. Instead, the programmer is forced to change how to express the computation, how to represent the data or how to do both, e.g., by flattening the matrix into a one dimensional array and use manual index computations, or by wastefully representing the data as a regular rectangular matrix filled with zeros.

As we will see, section 4 presents a type system that is capable of precisely capturing the shape of the triangular
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2.3 Summary

Current code generators do not support the generation of high performance code for computations with irregularly shaped multidimensional arrays. This section has motivated an extension of the type system in the existing Lift compiler. We have discussed two particular and quite different use-cases. As we will see in section 4, the extension of the type system increase the expressiveness by representing less regularly shaped multidimensional arrays while still imposing a structure that is exploited to generate efficient code.

The next section discusses the design of this extended type system which is inspired by a limited form of dependant types. We will first start by explaining the design of existing type system for regularly shaped multidimensional arrays.

3 Traditional Multidimensional Arrays

This section describes how traditional multidimensional arrays types are represented in existing code generators with functional intermediate representations like Accelerate, Futhark, or Lift that all track the shape or even size of multidimensional arrays in the type.

Tracking the shape (i.e., dimensionality) and size of arrays in the type system has proven useful for efficient code generation from functional intermediate representations. Accelerate tracks the shape of arrays in the type: Array sh a. Here sh represents the shape of the array and a the type of the array elements [13]. Futhark and Lift track the length of multidimensional arrays in the type system. In Futhark an array type is written: \([n]a\) where \(n\) is the number of elements and \(a\) the element type [11]. Similarly, but using a different notation, Lift expresses an array type as: \([A]n\) where \(A\) is the element type and \(n\) the number of elements [17].

To represent multidimensional arrays, both Futhark and Lift use nesting. A two dimensional \(n \times m\) matrix type is written as: \([A]n\m\), where \(A\) is the element type. Lift supports rich arithmetic expressions for the length of arrays beyond constants such as \(n\). Operations such as addition, division, or modulo are used to represent the length of arrays [18]. This paper extends the Lift type system which we discuss next.

3.1 Type System

The type system used by Lift is shown in Figure 2 using the formulation used by Atkey et al. [1] and adapted for the presentation here. We distinguish between three different kinds (2a): natural numbers (nat) for the length of arrays; data types (datatype) for types that are stored in memory; data types together with function types and a limited form of dependent function types make up the final kind (type). As types may contain variables we use a kinding judgement \(\Delta \vdash \tau : \kappa\) stating that type \(\tau\) has kind \(\kappa\) in the kinding context \(\Delta\). As our types contain expressions of natural numbers type equality can not be assumed by syntactic equality. Figure 2c
\[
\kappa ::= \text{nat} \mid \text{datatype} \mid \text{type}
\]
\[
\begin{align*}
\frac{x : \kappa \in \Delta}{\Delta \vdash x : \kappa} & \quad \frac{\forall \sigma : \text{dom}(\Delta) \rightarrow \forall \kappa, \sigma(x) = \sigma(y)}{\Delta \vdash I \equiv J : \text{nat}} \\
\end{align*}
\]

(a) Kinds

\[
\begin{align*}
\Delta \vdash N : \text{nat} & \quad \Delta \vdash M : \text{nat} \\
\Delta \vdash N + M : \text{nat} & \quad \Delta \vdash N \cdot M : \text{nat}
\end{align*}
\]

(b) Kinding Structural Rules

\[
\begin{align*}
\Delta \vdash \text{int} : \text{datatype} & \\
\Delta \vdash \delta \cdot \text{datatype} & \quad \Delta \vdash \delta_1 \times \delta_2 : \text{datatype}
\end{align*}
\]

(c) Type Equality

\[
\begin{align*}
\Delta \vdash \text{delta} : \text{datatype} & \\
\Delta \vdash \theta_1 : \text{type} & \quad \Delta \vdash \theta_2 : \text{type} \\
\Delta \vdash \theta_1 \rightarrow \theta_2 : \text{type} & \quad \Delta \vdash (x : \kappa) \rightarrow \theta : \text{type}
\end{align*}
\]

(d) Natural numbers

\[
\begin{align*}
\Delta \vdash \text{nat} : \text{datatype} & \\
\Delta \vdash \text{nat} : \text{type} &
\end{align*}
\]

(e) Data Types

\[
\begin{align*}
\Delta \vdash \text{nat} : \text{datatype} & \\
\Delta \vdash \text{nat} : \text{type} &
\end{align*}
\]

(f) Types

\text{Figure 2.} Well-formed Types of \texttt{Lift}

states that \text{nat}s are equal when their interpretations as numbers are equal for all interpretations of their free variables.

Figure 2d defines well-formed natural numbers which are either literals (indicated by \text{nat}) or expressions of natural numbers. We show here only addition and multiplication as possible binary operators, but in our implementation we support much richer expressions of natural numbers using additional operators such as division and modulo.

Figure 2e defines three different data types supported in \texttt{Lift}: scalar types such as \text{int}; array data types; and pair types. For each exists a direct representation in \texttt{C}, whereby pair types are mapped to structs. The design of the type system deliberately prevents function types inside arrays or pairs as OpenCL does not support function pointers.

Finally, figure 2f defines all well-formed \texttt{Lift} types. We consider all well-formed datatypes to be well-formed types and add the usual function type and a function type abstracting over data types and natural numbers at the type level.

### 3.2 Type Checking

The typing judgment \(\Delta \vdash P : \theta\) states that a program \(P\) is well typed with type \(\theta\) in the contexts \(\Delta\) and \(\Gamma\). For this the type \(\theta\) as well as all types in \(\Gamma\) must be well-formed by \(\Delta\) and \(P\) must be well-typed by \(\Gamma\). Figure 3 shows the typing rules of \texttt{Lift}. The structural rules in figure 3a show the forming of well-typed variables, implicit conversion between equal types, and how the primitives of \texttt{Lift} integrate. The rules in figure 3b are the standard \(\lambda\)-calculus rules for abstraction and application for usual lambdas as well as the \text{nat} and \text{datatype} dependent lambdas (written as \(\Lambda\)) where for application the argument is substituted in the type of the lambda body.

### 3.3 Computations over multidimensional arrays

The most important \texttt{Lift} primitives are shown in figure 4. We usually infer the first arguments representing \text{nat} and \text{datatype} and omit them when we write \texttt{Lift} programs.

The primitives nest naturally: \textit{map} applies a function to each array element - independent if the element is a scalar value or an array itself. Operations on higher dimensional data are expressible using familiar functional primitives. For example, matrix-matrix-multiplication can be expressed as:

\[
\text{map} \ (\lambda \ . r . \text{map} \ (\lambda c . \text{reduce} \ (+) 0 \ (\text{map} \ (+) \ (\text{zip} \ r \ c))) \ B) \ A
\]

While nesting of the presented array type enables the representation of regularly - or rectangularly - shaped multidimensional arrays, it is not sufficient to represent less regularly shaped arrays. In the two dimensional array type \([[\text{int}]]\_m\_n\), the inner size \(m\) must be the same for all elements of the outer array, as arrays are homogeneous containers.

Our goal is to relax this strict notion of homogeneity to allow differently shaped multidimensional arrays to be represented. But we still insist on some form of homogeneity for multidimensional arrays: the underlying scalar data type \(\text{int}\) in the example) must be the same for all elements in the multidimensional array. This ensures that arrays can be flattened efficiently in memory, e.g., with a C-like row-major storage layout. The information in the type: \(m, n\), and \text{sizeof} (\text{int}) is sufficient to compute the index of each element.

### 4 Position Dependent Arrays

This section describes the proposed extension to the \texttt{Lift} type system for irregularly shaped multidimensional arrays. We first describe the extended array type, followed by an
This is followed by a section on how to compute over data.

Multi-dimensional arrays are stored efficiently as a flat representation, where arrays are still homogeneous, besides for the array lengths. It is ensured that the underlying scalar type of the array position.

Figure 5 shows an array type where the element type is $\text{nat}$. Well-formed position-dependent array type

<table>
<thead>
<tr>
<th>$\Delta \vdash N : \text{nat}$</th>
<th>$\Delta \vdash i : \text{nat} + \delta : \text{datatype} $</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\Delta \vdash [\theta]_{1} \vdash [\theta]$</td>
<td>$\Delta \vdash [\theta]_{1} \vdash [\theta]$</td>
</tr>
<tr>
<td>$\Delta \vdash [\theta]_{1} \vdash [\theta]$</td>
<td>$\Delta \vdash [\theta]_{1} \vdash [\theta]$</td>
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</tr>
<tr>
<td>$\Delta \vdash [\theta]_{1} \vdash [\theta]$</td>
<td>$\Delta \vdash [\theta]_{1} \vdash [\theta]$</td>
</tr>
</tbody>
</table>

Figure 6. A triangular matrix value on the left and a type precisely describing it on the right.

\[
\begin{bmatrix}
\{\theta\}, \\
\{1, 2\}, \\
\{3, 4, 5\}, \\
\{6, 7, 8, 9\}
\end{bmatrix}
\vdash
i \mapsto [\text{int}]_{i+1} \{4, 5, 6, 7, 8, 9\}
\]

where

\[
s_0(i) = \begin{cases} 
2 & \text{if } i = 0 \\
1 & \text{otherwise} 
\end{cases}
\quad \text{and} \quad s_1(i, j) = \sum_{k=0}^{i-1} s_0(k) + j + 1
\]

Figure 7. A three-dimensional array, irregularly grouping rows of a triangular matrix. Type shown on the right.

instance, the expression of a type of a matrix which stores floats in the same rows and doubles in some other rows, as it wouldn’t be clear how to compute efficiently the addresses of the individual elements in memory.

4.2 Example

To describe the type of a triangular matrix, we write: $\lbrack i \mapsto [\int]_{i+1} \rbrack_{n}$. This type indicates that the length of each row is equal to its position $i$ in the array plus one (to accommodate the 0-based indexing): the first row has length 1, the second row has length 2, and so on with the last row (at
position $n-1$) having length $n$. This is shown in figure 6 for a two dimensional array with four nested arrays of different size. Overall the data forms a lower triangular matrix that is reflected in its type.

**Triangular Matrix** For the triangular matrix example with the type $[i \mapsto \text{int}[i+1]]_n$, $\delta$ is the nested type $\text{int}[i+1]$ for which the length depends on $i$ and can be described by this function: $s(i) = i+1$. This is a strict generalization of the classical array type seen in the previous section. If the element type $\delta$ does not depend on $i$ then all elements of the arrays must have the exact same length, as for arrays with a classical array type. For example, a two dimensional matrix can be expressed in our extended array type as: $[i \mapsto \text{int}_m]_n$. If the array index $i$ is not used in the type we might omit it: $[- \mapsto \text{int}_m]_n$. This type is equivalent to the classical multidimensional array type: $[[\text{int}_m]_n]_n$.

**Higher Dimension Arrays** For higher dimensional types, the sizes of nested arrays might depend on all positions of the surrounding arrays. See figure 7 for an example. Here the first two rows of a triangular matrix have been grouped together forming a nested array together with the third and fourth row. Such a representation could be useful to achieve some form of load balancing by grouping multiple shorter rows together to balance the number of elements in every group. The three dimensional type is interesting with two functions $s_0$ and $s_1$ describing the size of the nested array dimensions. For $s_0$ a case statement is used to define the function specifying that the first element of the outer array will have two elements while the other elements will all be of size one. The deeper nested array has a more complex computation of its size. $s_1$ depends on both positions $i$ and $j$ of the surrounding arrays. We can still see the same arithmetic expression used to represent the triangular matrix: $j + 1$. In addition, the expression $\sum_{k=0}^{i-1} s_0(k)$ computes a prefix sum over the outer dimensions expressed in $s_0$. We will understand how to derive this expression from the triangular matrix type using a new primitive we will introduce in section 4.4 called partition.

### 4.3 Computations over irregularly shaped arrays

So far, we have seen how to represent irregularly shaped multidimensional arrays with a novel type. We will now investigate how to express computations over such data structures in the functional high-level notation of Lift. We will use as much of the existing Lift primitives as possible.

The Lift primitives shown before in figure 4 are overloaded to work on the new position dependent arrays as shown in figure 8 for this the type system is extended with type level functions that map natural numbers to data types (nat $\rightarrow$ datatype) or to natural numbers (nat $\rightarrow$ nat).

The types of the primitives using the position dependent array types are interesting. For map the elements in the input array are now described by the type level function $f_{\delta_i}$ mapping natural numbers to datatype. The first value of the array has type $(f_{\delta_0} 0)$ the second $(f_{\delta_0} 1)$ and so on. The function that map applies to each element of the input array is now parameterized by an additional natural number $k$ that represents the index at which the function is applied. The $k$-th element of the input array has the type $(f_{\delta_0} k)$. A similar type level function $f_{\delta}$ describes the element types in the output array. reduce, zip and join generalize to position dependent arrays in a similar way to map.

We do not provide a version of split for position dependent arrays, as we will introduce a new primitive in the next section called partition, which is more general than split.

Using the overloaded Lift patterns together with the extended array type we can straightforwardly write the implementation of triangular matrix multiplication, as shown in listing 3. We start by applying map to the triangular matrix to perform a computation for every row. For each row we compute the dot product with the vector by combining them with zip, multiplying the resulting pairs and summing them up. Lift transforms high-level programs into efficient low-level code by applying a set of rewrite rules in an automated optimization process. This process rewrites the expression by, e.g., fusing patterns to avoid the generation of unnecessary temporaries and by mapping the computation to the different levels of parallelism offered by modern hardware.

The only difference compared to the matrix vector multiplication of a rectangular matrix is the use of slice and getLength. Slice is a pattern for accessing a subarray defined by start and end indices. It is implemented in terms of a more general pattern called partition, whose details are covered in.

---

**Listing 3. Lift code for triangular matrix multiplication**

```
fun (matrix : nat $\rightarrow$ [float[i+1]])_N, vector : [float[N]] =>
  map ($\lambda$ row $\rightarrow$ reduce (add, 0, map (mult, zip (row, slice (0, getLength (row), vector)))))
```

---
As mentioned before, partition can be seen as a generalization of split. There exists a few rewrite rules that include split, like a divide-and-conquer style rule that splits an input array into several parts that are then processed individually before the results are joined back together:

\[
\text{map}(f, \text{input}) \mapsto \text{join}(\text{map}(f), \text{split}(n, \text{input}))
\]

These rules can simply be generalized by exchanging partition for split, e.g., to express a load balancing aspect when the work of applying \( f \) to every element of the input array is not uniformly distributed. In addition to these generalized rules, it is possible to express a more specific low level optimization: the use of partition for a more fine-grained handling of stencil boundary conditions.

As seen in section 2.2, stencil applications need to handle the boundary of its multidimensional input array specially, for example by padding the array with additional values or (as shown in listing 1) by clamping the index computation. This boundary handling introduces potentially expensive branches in the code, human experts often write their programs in such a way to handle these section as special corner cases.

Due to the uniformity of the \( \text{Lift} \) primitives and its regular array types, it is not possible to express this optimization without support for position dependent arrays. The introduction of partition, however, allows for the optimized handling of boundary conditions to be expressed and automatically introduced via a rewrite rule:

\[
\text{map}(f, \text{slide}(\text{size}, \text{step}, \text{pad}(l, r, \text{input}))) \mapsto \\
\text{join}(\text{map}(f), \text{partition}(3, \text{caseSplit}(l, n - l - r, r), \\
\text{slide}(\text{size}, \text{step}, \text{pad}(l, r, \text{input}))))
\]

The intuition behind this rule is as follows: we insert a partition right before executing \( f \) which represents the stencil computation performed over the stencil neighborhood. The partition splits the grid of neighborhoods that has been produced by slide in three distinct sections: a prologue, a central body, and an epilogue. The sizes of the prologue and epilogue correspond to the number of elements padded to the input. The central body takes up the remaining input size.

Due to the information available in the type the compiler is capable of removing the out-of-bound checks from the central body section of the code. We will discuss details how this is implemented in section 5.

Concerning the rule correctness, we can see that the rule is indeed semantic preserving: partition has the effect to splitting the input in chunks and add one layer of nesting. The addition of a subsequent map wrapping around the original body does not modify the number or order of the elements in the output, but simply influences how the computation is organized. Finally, the terminating join will undo the effects of partition on the output.

4.5 Summary

In this section we have introduced an array type that allows for the size of nested arrays to depend on their position in the outer array. This enhances the expressiveness allowing to represent data structures such as triangular matrices or
trees. We have seen that computations over such structures are as naturally expressed using the same set of primitives already familiar to functional programmers.

Furthermore, we have introduced a new primitive to partition a regular array into a nested irregular one and we have discussed how an rewrite rule automatically exposes this transformation as an optimization choice for removing unnecessary boundary checks. In the next section we will discuss important implementation details.

5 Implementation and Code Generation

After describing the design of the extended multidimensional array type in the previous section we now discuss some of the implementation details. We first describe the implementation challenges faced, before discussing them individually. Finally, we will briefly discuss the code generation before evaluating the performance achieved in the next section.

5.1 Implementation challenges

Extending the existing Lift OpenCL backend to support the extended array type presented a number of challenges:

- Allowing for the array size to depend on its position in the surrounding arrays significantly complicates the computation of the number of element in a multidimensional array. This is a fundamental operation necessary for computing array indices and to perform memory allocation.
- The implementation of efficient index computations in the generated OpenCL kernel is no longer straightforward. When done naively, many of the generated arithmetic expressions would require the introduction of loops and conditional branches when computing indices.
- The implementation of partition should not produce results directly but instead lazily influence the code generated for following patterns. We describe a solution using Lift’s view system.
- Finally, we will discuss some memory management and memory allocation challenges.

5.2 Calculating the Number of Array Elements

In the previous section, we have seen how Lift provides support for multidimensional arrays, and that these arrays are essential to the compositional nature of Lift programs. Multi-dimensionality in Lift, however, exists purely as an abstraction: in order to generate high performance code, the OpenCL code generator flattens the multidimensional arrays into contiguous memory buffers.

This requires the compiler to calculate the number of elements contained in a potentially multidimensional array. In the context of regular arrays with a classical type, one can easily compute the linear index using the formula

\[ \text{dim}_1 \times \text{dim}_2 \times \ldots \times \text{dim}_n \]

For an irregular sized arrays with an extended array type, the number-of-elements formula generalized as follows:

\[ \sum_{i_1=0}^{\text{dim}_1-1} \ldots \sum_{i_{n-1}=0}^{\text{dim}_{n-1}-1} \text{dim}_n(i_1, \ldots, i_{n-1}) \]

An important feature of the Lift compiler is the extensive use of symbolic algebra for reasoning about arithmetic expressions of natural numbers, such as array sizes and iteration ranges. The system was originally designed to only work with arrays of regular length and is introduced and described in [18]. With the introduction of the extended array types, it becomes, therefore, necessary to extend the symbolic algebra system to include a new \( \Sigma \) construct. This construct corresponds to the concept of an algebraic summation as commonly used in mathematics. We discuss next how we exploit the properties of algebraic summations to optimize many cases of index computations.

5.3 Optimizing Index Computations

Generating concise index computations is incredible important for achieving high performance. Prior work [18] reports massive performance losses for applications such as matrix matrix multiplication when index computations are not simplified by the compiler. In this section we describe the generation of optimized index computations that contain the newly introduced \( \Sigma \) operator.

A possible naive implementation of \( \Sigma \) would generate a sequential loop. Due to performance considerations however, this approach is in practice not viable. Instead, in the majority of cases encountered in practice, the index computation can be simplified to a close form formula without any further \( \Sigma \) terms.

**Example** Consider the problem of indexing of an element of a triangular matrix flattened in memory. As we saw earlier, the matrix has type \([ i \mapsto [\delta]_{i+1} \] \), which means the length of each row is \( i + 1 \). To compute the position in memory of element \((\text{rid}, \text{cid})\), we can compute a close form as follows using well-known properties of \( \Sigma \):

\[
\text{memLocation}(\text{rid}, \text{cid}) = \left( \sum_{i=0}^{\text{rid} - 1} i + 1 \right) + \text{cid} \\
= \text{rid} + \left( \sum_{i=0}^{\text{rid} - 1} i \right) + \text{cid} = \text{rid} + \frac{(\text{rid})(\text{rid} - 1)}{2} + \text{cid} \\
= \frac{(\text{rid} + 1) \times \text{rid}}{2} + \text{cid}
\]

By implementing these algebraic simplification rules, the compiler is capable of generating efficient index computations for a large number of expressions. The following section list the rules implemented in the compiler.
Simplification rules for $\Sigma$ The rules presented here might look slightly different from their usual presentation in mathematics: since in this work their main use is to determine offsets in linear arrays, the rules are indexed from 0, as opposed to the more commonly used indexing from 1.

\[
\sum_{i=0}^{N-1} i = \frac{N \times (N-1)}{2} \quad (2)
\]

\[
\sum_{i=0}^{N-1} 2^i = 2^{N+1} - 1 \quad (3)
\]

\[
\sum_{i=0}^{N-1} f(i) = \sum_{i=0}^{b} f(i) - \sum_{i=0}^{a} f(i) \quad (4)
\]

\[
\sum_{i=0}^{N-1} c \times f(i) = c \times \sum_{i=0}^{N-1} f(i) \quad (5)
\]

\[
\sum_{i=0}^{N-1} f(i) + g(i) = \sum_{i=0}^{N-1} f(i) + \sum_{i=0}^{N-1} g(i) \quad (6)
\]

\[
\sum_{i=0}^{N-1} f_{1}(i) \quad \begin{cases} f_{2}(i) & \text{if } i = c \\ f_{2}(i) & \text{otherwise} \end{cases} = \sum_{i=0}^{N-1} f_{1}(i) + \sum_{i=0}^{N-1} f_{2}(i) \quad (7)
\]

The algebraic simplification rules can be roughly grouped in three categories, according to their main purpose:

- Rules (1) - (3) are used to compute the number of elements of one dimension of an irregular array, each matching a different primitive shape that the dimensions can take. (1) is used for fixed size dimension, (2) corresponds to linearly variable dimension, such as the rows of triangular matrices (3) corresponds to exponentially variable dimension, such as binary trees.
- Rules (4) - (6) are auxiliary simplification rules, used to split composite sums into simpler parts.
- Rule (7) deals with the elimination of if-statements, usually generated by the length function of partition. This rule is an analogue of loop peeling, a classical compiler optimization in which loop iterations are extracted out of the loop into the loop’s prologue and epilogue.

If-elimination rules We observe that programs containing non-trivial uses of partition could generate inefficient OpenCL programs. The main cause of this is the presence of a large number of conditional expressions - many generated by the loop-peeling simplification mentioned above.

To address this issue, we investigated if-elimination rules in the arithmetic expressions. By leveraging the strength of the LIFT symbolic algebra system, which tracks the ranges for each arithmetic expression. It is possible to implement a simplifier that is often capable of identifying the conditional expressions that are guaranteed to be never taken. This is achieved by checking the difference between the minimum and maximum possible values of the expressions within the conditional. For example, a conditional of the form

\[
\begin{cases} x & \text{if } a \geq b \\ y & \text{otherwise} \end{cases}
\]

simplifies to $x$ if $\min(a) \geq \max(b)$, and to $y$ if $\max(a) < \min(b)$. Similar rules exist for other boolean operators.

5.4 Implementation of Partition

Some LIFT primitives are lazy: instead of performing a computation and writing into memory, they influence the behavior of the following patterns, by creating a compiler intermediate data structure — called a view — over their inputs and outputs. An example of a lazy pattern is split that reshapes an input array by introducing another dimension. The partition also primitive falls into this category as it lazily influences the reading of memory of following patterns.

The rules presented here might look slightly different from their usual presentation in mathematics: since in this work their main use is to determine the best parameter configuration we found.

5.5 Code Generation

After addressing the challenges described here, there is no need to modify the LIFT code generator which remains unchanged compared to the techniques described in [1].

5.6 Summary

In this section we have discussed a number of implementation challenges and how we overcome them. Particularly, the introduction of the extended array type has lead to changes in computing length of and indices into arrays. By introducing and optimizing $\Sigma$ as arithmetic expressions, we are able to generate low level code from familiar high level LIFT expressions. The new partition primitive is implemented as a LIFT view and maybe surprisingly, no other modification to the LIFT code generator was necessary for implementing our work. The next section experimentally evaluate the implementation using the two case studies introduced earlier.

6 Experimental Evaluation

Experimental Setup We conducted an experimental evaluation using single precision floats on a GeForce GTX TITAN X with CUDA 8.0 and driver version 375.66. We report the median of at least 100 executions measured using the OpenCL profiling API. Data transfer times are ignored since the focus of the evaluation lies on the quality of the generated kernel code. For the triangle matrix vector benchmarks, we perform an automatic exploration of implementation parameters including the OpenCL local size. We report the runtimes for the best parameter configuration we found.
Listing 4. Lift code for the Basic implementation of triangular matrix-vector multiplication

```
fun(matrix:[i→[float]_{i+1}])N, vector:[float]N) → (1041
  mapGlobal(λrow → (reduceSeq(λ(acc, t) → acc+(t.0*t.1)) (1042
    zip(row,slice(N,0,getLength(row))(vector))) ) (matrix) ) )
```

Listing 5. Lift code for the Best implementation of triangular matrix-vector multiplication

### 6.1 Triangle Matrix Vector Multiplication

We start with the triangular matrix vector multiplication for which we saw already the high level Lift code in listing 3.

**Lift implementations** We present two different versions of the triangular matrix vector multiplication in Lift. The first one is derived from the high-level implementation, referred to here as basic version. The other is an improved version, written to better exploit the parallel facilities of the GPU. This version is referred to as the best.

The code for basic is shown in listing 4. The program then follows the structure of a simple high-level matrix-vector multiplication. Different to the high level Lift program in listing 3 this version includes the OpenCL specific parallel versions of map indicating the parallelism mapping. Global threads are used to process each row of the matrix in parallel.

The only divergence from a regular matrix vector multiplication lies in the `slice(N,0,getWidth(row))` expression, which clips the vector to the length of the current row.

The code for best is shown in listing 5. In this version, we assign each row to a workgroup and then, instead of clipping the vector, we extend the row up to the vector length using the `padConstant` primitive. The `padConstant` primitive creates a view to lazily extend the array with a constant value. This allows us to further split the row and column vectors and process each chunk in a separate thread.

One must take note that the code for best presented here comprises only the first part of the algorithm. The code shown in listing 5 computes a partial reduction for each row. A second reduction kernel is then necessary. As this is a common feature of many high performance GPU applications, the code is omitted. The runtime cost of the second kernel, while negligible, is included in the results.

**Generated OpenCL code** Listing 6 and listing 7 show the automatically generated OpenCL codes. The outer for loop of the basic version distributes the rows across the global threads. The input index in lines 15 - 17 is automatically derived from the extended array type. It is concise following the optimizations described in section 5.

Listing 6. OpenCL code for Lift basic implementation of triangular matrix-vector multiplication.

```
kernle void BASIC(const global float* restrict matrix, const global float* restrict vector, global float* out) {
  float acc = 0.0f;
  for (int row_idx = get_global_id(0); (row_idx < N);)
    row_idx = row_idx + get_num_groups(0) {
      split_idx = split_idx + get_local_size(0) {
        for (int i = 0; i < SPLIT_SIZE; i = 1+i) {
          (((SPLIT_SIZE * split_idx) < 0) ||
            ((SPLIT_SIZE * split_idx) >=
              (SPLIT_SIZE * split_idx)))
            || vector((i + SPLIT_SIZE * split_idx))/2) +
            out[(split_idx + i) * SPLIT_SIZE] = id(accum); }
          (row_idx = row_idx + (row_idx * row_idx)) / 2)));
        vector[i]; )
      out[row_idx] = id(accum); } )
    split_idx = split_idx + get_local_size(0) {
      for (int i = 0; i < SPLIT_SIZE; i = 1+i) {
        ((SPLIT_SIZE * split_idx) < 0) ||
          ((SPLIT_SIZE * split_idx) >=
            (SPLIT_SIZE * split_idx)))
        out[slice((N-row_idx)/SPLIT_SIZE) = id(accum)]; }
      (row_idx = row_idx + (row_idx * row_idx)) / 2)) +
    out[row_idx] = id(accum); } )
```

Listing 7. OpenCL code for Lift best implementation of triangular matrix-vector multiplication.

```
kernle void BEST(const global float* restrict matrix, const global float* restrict vector, global float* out, int N) {
  float acc = 0.0f;
  for (int row_idx = get_global_id(0); (row_idx < N);)
    row_idx = row_idx + get_num_groups(0) {
      split_idx = split_idx + get_local_size(0) {
        for (int i = 0; i < SPLIT_SIZE; i = 1+i) {
          (((SPLIT_SIZE * split_idx) < 0) ||
            ((SPLIT_SIZE * split_idx) >=
              (SPLIT_SIZE * split_idx)))
            || vector((i + SPLIT_SIZE * split_idx))/2) +
            out[(split_idx + i) * SPLIT_SIZE] = id(accum); }
          (row_idx = row_idx + (row_idx * row_idx)) / 2)));
        vector[i]; )
      out[row_idx] = id(accum); } )
```

The best version’s alternative parallelization strategy is more complicated: the SPLIT_SIZE parameter in the Lift code controls the amount of work in each workgroup.

**Performance Results** Figure 10 presents the performance results measured for the triangular matrix vector multiplication expressed in Lift and compared against the equivalent BLAS kernel `trmv` implemented in cuBLAS. cuBLAS is the fastest known high performance linear algebra library for Nvidia hardware. As we can clearly see, the Lift generated code outperforms the `trmv` cuBLAS implementation clearly on all input sizes. The best Lift version is also significantly faster than the basic version. The largest input size represents a large triangular matrix of over 500 MB and when approaching this size, the performance of cuBLAS improves and the advantage of the Lift generated code becomes smaller. Still, the Lift generated code outperforms cuBLAS by up to 2.3X.

### 6.2 Boundary Conditions of Stencil code

In the second case study, we show how the introduction of irregular arrays is used as a means to express a low level optimization by considering the case of boundary checking in stencil codes. Stencil codes need special handling at the boundary, for example clamping array accesses with a check and re-index computation when out-of-bound. This handling requires the introduction of branches, human experts often prefer to write extra code for handling the boundary regions.
Listing 8. Jacobi stencil expressed in Lift

```c
fun(input: [:float] [N], boundary: float) → {
    join o mapSeq(mapGlobal((reduceSeq(add, 0) o join))) o
    slide2D(STENCIL, 1) o
    pad2D(STENCIL/2, STENCIL/2, boundary)(input)
}
```

Listing 9. Jacobi stencil expressed in Lift with specialized boundary handling

```c
for (int i = get_global_id(0); i < N;)
    i = (i + STENCIL_SIZE(0))
for (int j = 0; j < STENCIL_SIZE; j = 1 + j)
    acc = add(acc, input[(i + j)])
```

Listing 10. OpenCL code generated for a one-dimensional stencil without special handling of boundary conditions.

#### Classical approach
Listing 8 shows a classic Lift implementation of a 2D Jacobi stencil. The program contains of three main steps: first, the input grid is padded, which is Lift’s way of introducing specialized boundary handling. Next, slide2D is used to create an array of neighborhoods. Finally, the code within mapGlobal implements the actual stencil computation performed on each neighborhood. The main issue with this straightforward implementation lies in the result of pad2D, which in Lift is a view over the padded input array. Therefore, every access into this array needs to be guarded by boundary checks, resulting in index expressions with conditionals. But, these checks are only necessary for neighborhoods with elements falling outside the boundary. The use of regular arrays in the Lift expression prevents us to be able to express this specialized behavior.

#### Position dependent array approach
We can solve this problem by applying the rewrite rule presented in section 4.4 that introduces the `partition` primitive. The code for the rewritten Lift program is shown in listing 9. In this version, a `partition` call has been introduced, splitting the input to the stencil in three - unequally sized - areas: left boundary, center, and right boundary. Since `partition` has a known constant number of partitions, the code generator will not produce a for-loop when mapping over it, but instead fully unroll it. This will yield three different sections, corresponding to the prologue, body and epilogue of the stencil computation. Moreover, since the Lift compiler accurately tracks the ranges of iteration variables, it also infers that the prologue and epilogue sections are implemented with an if instead of a for loop, since there is at most one iteration per thread.

The effects of this transformation on the generated OpenCL code are visible by comparing the code snippets shown in listing 10 and listing 11. For sake of clarity, we are showing the code for a one-dimensional stencil: the principle is the same as for the 2d stencil used in the experimental evaluation one with additional loops in the OpenCL code and more complex index computations.

The traditional Lift stencil code has a single nested loop that is performing the entire computation. Every access into the input performs the costly out-of-bound checks.

In the rewritten stencil the computation has been split in three separate code sections, where only the prologue and epilogue containing the boundary checks. This is possible, as the arithmetic expression simplifier automatically infers closer bounds for the loop variables guaranteeing that such checks are redundant for the body of the stencil. It is therefore safe to omit them.

#### Performance Results
Figure 11 presents the performance impact of applying the rewrite rule on a 2D Jacobi stencil when we vary the size of the stencil. The bars represent the speedup of the rewritten version compared with the non-rewritten baseline for a variety of stencil sizes. The data shown is relative to an grid-input size of 4096x4096 floating
Dependent Types  

Dependently typed languages such as the earlier Epigram [12] or the more modern Idris [4] possess type systems capable of encoding and enforcing complex properties over values in the type. This might include properties concerning the shape of data structures, which enable dependently typed programs to naturally express irregular data structures.

This great expressive power comes however at a cost, and efficient compilation of dependent type languages is an active area of research. We use a limited form of dependent types in this work for a domain-specific purpose: to describe parallel computations and to generate efficient parallel code.

Irregular structures in linear algebra applications  

The linear algebra community has seen the development of a number of approaches to produce efficient implementations for complex linear algebra problems, such as the FLAME methodology [9], a systematic way for deriving parallel algorithms for linear algebra operations, and Linnea [2], a rule based rewrite engine capable of generating efficient implementations of complex linear algebra expressions from a high-level mathematical expression.

The work presented in this paper would allow Lift to serve as a high-performance code generator for high level programs derived using such tools.

8 Conclusions  

This paper has presented an extension to classical array types. While existing functional code generators already track the size of arrays in the type, this is overly restrictive and prevents useful data structures such as triangular matrices or trees to be represented precisely as types. In this paper we have shown how to design an extended array type with a limited form of dependent typing that allows for nested array sizes to depend on their position in the surrounding array.

We have shown our practical implementation as an extension of the Lift compiler and presented the implementation challenges mostly related to index simplification. We have demonstrated that this approach enables the efficient code generation of triangular matrix vector multiplication, with performance improvements over cuBLAS on an Nvidia GPU by up to 2×. We have discussed a use case for representing and implementing a low level optimization for avoiding out-of-bound checks, leading to up to 3× performance improvement over already optimized stencil codes.

In the future, we would like to further extend our array type, exploring the practical implications of representing tree data structures in a packed memory representation, as common in computer graphics applications.

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References


